

Practitioner's Docket No.: 013176.0403D1C1 (Formerly 13176.403D1US (.459))

Patent Application

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of: Solayappan et al.

Group Art Unit: Unknown

Serial No.: 10/695,016

Examiner: Unknown

Filed: October 28, 2003

For: STACKED MEMORY CELL AND PROCESS OF FABRICATING SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop Non-Fee Amendment
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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In accordance with 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is directed to the cited references listed on the attached PTO/SB/08A and PTO/SB/08B. The cited references are believed to be the most relevant known to Applicants and/or Assignee at this time concerning the invention as claimed in the above-captioned patent application. No representation is made or intended that more relevant information does not exist or that the order of presentation of the information in any way reflects their relative pertinence.

Applicants respectfully request that each of the cited information be expressly considered during the prosecution of this application and that the cited references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. A copy of each reference is attached.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Action or Notice of Allowance. 37 C.F.R. §1.97(d).

I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. §1.97(e)(1). Thus, it is believed that no fee is required. However, if necessary, please charge any deficient fees to Deposit Account No. 50-1848.

Respectfully submitted,
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CERTIFICATE OF MAILING (37 C.F.R. 1.8(a))

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PTO/SB/08a (05-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/695016
Filing Date	28-Oct-2003
First Named Inventor	Narayan Solayappan
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	013176.0403D1C1 (Formerly 13176.403D1US (.459))

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US- 6,121,648	09-19-2000	Joseph T. Evans, Jr.	
	2	US- 2001/0006241 A1	07-05-2001	Bee-Lyong Yang	
	3	US- 2001/0034106 A1	10-25-2001	Moise et al.	
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FOREIGN PATENT DOCUMENTS

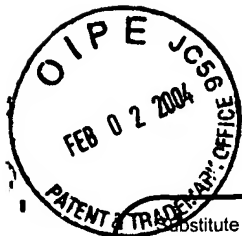
Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	4	EP 0 412 185 A1	02-13-1991	Dr. Josef Willer		
	5	JP 11121704	04-30-1999	Watabe Koji		
	6	EP 0 915 522 A2	05-12-1999	Kazushi Amanuma		
	7	EP 1 113 493 A1	07-04-2001	Dehm et al.		

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Attorney Docket Number	013176.0403D1C1 (Formerly 13176.403D1US (459))

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	8	GODA ET AL., "A Novel Surface-Oxidized Barrier-SiN Cell Technology to Improve Endurance and Read-Disturb Characteristics for Gigabit NAND Flash Memories", IEEE, 2000, pp. 33.3.1 - 33.3.4	

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